Short Curriculum Vitae Dr. Manfred Dietrich

Education
1958 - 1966 primary school: Polytechnische Oberschule Müllrose

1966 - 1970 high school: Erweiterte Oberschule Eisenhüttenstadt (Abitur)

1966 - 1970 Skilled Worker Training Electrican (Elektromonteur)

1970 -1974 Study Information Technology at TU Dresden

1974 Diploma as Dipl.-Ing. with thesis „Numerical Classification for speech recognition"

1974 - 1978 Scientist at the University of Wismar

1982 Defense of the doctor's thesis „Fault Location in analog Circuits"

Profession
1978 - 1991 Design Engineer and group leader at Halbleiterwerk Frankfurt (Oder): Development of EDA-Tools

1991 - 2003: Scientist and deputy of department „Microelectronic" at the project agency of the education and research ministry of Germany at the Deutsches Zentrum für Luft- und Raumfahrt"

1997- 1999 Scientist Consultant at the Federal Ministry of Education and Research (BMBF)

2003 - 2017 Scientist, Head of department and head of Research Planning at the Fraunhofer Institute for Integrated Circuits

1994 - 2016 Shareholder and director of the company TexEDA Design GmbH (early: Catena Software GmbH)

Seit 2017: project coordinator at TU Dresden

Seit 2017: independent business consultant (DiKuLi Unternehmesberatung)

Awards
2017: EDA-Medaille

2018: Erinnerungsmedaille der Hochschule Für Technik und Wirtschaft in Dresden

Gremien
Member of the program committee of the IEEE International Symposium on Diagnostic and Design of Electronic Circuits and Systems DDECS

Member of the Steuerungsgremium der MIkrosystemtechnik-Tagung

Member of the program committee oft he VDE-Tagung „Zuverlässigkeit und Entwurf"

Member of the joint working goup oft he VDE and the GI „Rechnergestützter Schaltungs- und Systementwurf"

Member of the committee for the European ECSEL Strategic Research Agenda

Member of the program committee of the Eureka-AENEAS conference "Nanoelectronics, Applications, Design & Technology Conference ADTC"

Member of the program committee of the conference "EDA-Workshop"

Member of the edacentrum e.V.

Member of the GFWW e.V.

Books
Manfred Dietrich, Jürgen Haase: Process Variantions and Probabilistic Integrated Circuits, Springer New York Dordrecht Heidelberg London 2012, ISBN: 978-1-4419-6621-6

Jens Lienig, Manfred Dietrich: Entwurf integrierter 3D-Systeme der Elektronik, Springer Vieweg, ISBN: 978-3-642-30571-9